

Docket No: 244186US2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

:

Satoshi INOUE

: EXAMINER:

SERIAL NO: 10/687,629

:

FILED: October 20, 2003

: GROUP ART UNIT: 2812

FOR: PROCESSOR AND SEMI-
CONDUCTOR ...

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FILING OF SUPPLEMENTAL APPLICATION DATA SHEET

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the purpose of inserting a residence address for the inventor.

Respectfully submitted,

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APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Number:: 10/687,629
Application Date:: 10/20/03
Application Type:: REGULAR
Subject Matter:: UTILITY
CD-ROM or CD-R?: NONE
Title:: PROCESSOR AND SEMICONDUCTOR
INTEGRATED CIRCUIT
Attorney Docket Number:: 244186US2
Total Drawing Sheets:: 8

INVENTOR INFORMATION

Applicant Authority Type:: INVENTOR
Primary Citizenship Country:: Japan
Status:: FULL CAPACITY
Given Name:: Satoshi
Family Name:: INOUE
City of Residence:: Chiba-shi
State or Province of Residence:: Chiba-ken
Country of Residence:: Japan
Street of Mailing Address:: c/o Intellectual Property Division, Toshiba
Corporation, 1-1, Shibaura 1-chome,
Minato-ku,
City of Mailing Address:: Tokyo
Country of Mailing Address:: Japan

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2003-159174	Japan	06/04/03	YES

ASSIGNMENT INFORMATION

Assignee Name::

KABUSHIKI KAISHA TOSHIBA

Street of Mailing Address::

1-1, Shibaura 1-chome, Minato-ku,

City of Mailing Address::

Tokyo

Country of Mailing Address::

Japan